KERALA PUBLIC SCHOOLS

HOME ASSIGNMENT (01st To 4th July 2020)



CLASS	SUBJECT	TOPIC / CHAPTER	MODULE / ASSIGNMENT	REFERENCE LINKS
XII	BIOLOGY	Evolution	1. Explain Oparine –Haldane Theory	
		1 st -17 th July	2. Explain Urey – Miller's Experiment.	Balaii Publication
			3. Define – Protobionts, Coacervates, Vestigeal Organs	ISC Biology
			4. Differentiate between Homologous and Analogous Organs	
			5.Explain what is meant by embryological evidence.	https://youtu.be/hj3Rhf2vlZg
			6.Explain what is meant by molecular evidences in evolution.	https://voutu.be/AxHnrBIsHtO
			7. Explain Larmarckism.	<u>intpolityoutdoor interformed on the control of the</u>
			8.Explain Darwinism.	
			9.Explain Lederberg 's replica plating technique.	
			10. What is Hardy Weinberg's Principle?	
			11 Explain the three types of Natural Selection.	
			12 Explain the characteristics of Neanderthal man .	
			13. Explain gene flow.	
			14 Define genetic drift.	
			15 What is Founder's effect and Bottle neck effect.	
		•	<u> </u>	
	HINDI LANG	एक फूल की चाह,	एक फूल की चाह:-	
		उधमा गर	1. बीमार बेटी ने क्या इच्छा प्रकट की और क्यों?	
			2. सुखिया का पिता अपनी बेटी की इच्छा क्यों नहीं पूरी कर सका?	
			3. सुखिया के पिता पर क्या आरोप लगाया गया और क्यों?	

		 4. कवि ने समाज की किस बुराई को कविता के माध्यम से उजागर किया है? 5. इस कविता से आपको क्या प्रेरणा मिलती है? 6. मंदिर के सौंदर्य का वर्णन अपने शब्दों में करें। उद्यमी - नर:- 1. "उद्यमी नर" कविता का केन्द्रीय भाव लिखें। 2. संसार के उद्यमी नर ही सुख पाते हैं, कैसे? 3."जिसने श्रम जल दिया उसे पीछे मत रहने दो।" स्पष्ट करें 4. प्रकृति को कौन हरा सकता है और कैसे? 5. उद्यमी नर कविता के माध्यम से नर को क्या संदेश दिया गया है? 6. यह कविता प्रगतिवादी भावना से ओत - प्रोत है, स्पष्ट करें। 	
COMPUT ER SCIENCE	1-07-2020 Ch : Proposition Logic & Boolean Algebra	 *The assignments given below have to be done in the Computer Science notebook. 1. Draw the truth table to prove the proportional logic expression. (X => Y) ∧ (Y => X) = X <=> Y 2. Prove the following relation: [ISC 2018] (P ^ Q) V (P ^ ~ Q) = P 3. State the following expression is a Tautology, Contradiction or the Contingency with the help of truth table. [ISC 2016] (X => Z) V ~(X => Y)^{ (Y => Z) } 4. Using the truth table, state whether the following proposition is a <i>tautology</i>, (A ^ B) V (A => B) 5. The statement are given as: If P: Jitendra Kumar is a computer teacher. Q: Rahul is a student. (b) Jitendra Kumar is a computer teacher and Rahul is a student. (c) Jitendra Kumar is not a computer teacher and Rahul is a student. (d) Neither Jitendra Kumar is a computer teacher nor Rahul is a student. (e) Either Jitendra Kumar is a computer teacher or Rahul is a student. (e) Either Jitendra Kumar is a computer teacher or Rahul is a student. (d) Neither Jitendra Kumar is a computer teacher or Rahul is a student. (e) Either Jitendra Kumar is a computer teacher or Rahul is a student. 	Ch: Propositional Logic & Boolean Algebra Topic: Propositional Logic https://www.youtube.com/watc h?v=Y4KtBizkUn4&list=PLGs RpMr9gXDdKpoy6dgve3n2q_ xcKxvLn Topic: Logic Gates and Boolean Algebra https://www.youtube.com/watc h?v=zfMxkjOtCws

		7 State the two Indemnotence law of Boolean algebra. Verify any of them in	
		y: State the two indempotence has of Boolean argeora. Verify any of them in	
		$ \begin{array}{c} \text{using truth table.} \\ \text{[2]} \\ \text{S} \text{Find the deal of} \end{array} $	
		8. Find the dual of:	
		(A'+B).(I+B') = A'+B	
		9. Find the maxterm and minterm when [2]	
		P = 0, Q = 1, R = 1 and S = 0	
		10. Convert the following Boolean expression into its canonical [3] $F(A,B,C) = (B + C') \cdot (A' + B)$	
		11. Verify the following expression using Boolean laws. Also mention the law used	
		at each step of simplification. $\mathbf{V} \cdot \mathbf{V} = \mathbf{V} \cdot \mathbf{V} \cdot \mathbf{V} = \mathbf{V} \cdot \mathbf{V} \cdot \mathbf{V}$	
		A. I. $L + A$. I. $L + A$. I. $L = A(I + L)$ 12 Civen the Declean function $\mathbf{F}(A, \mathbf{P}, \mathbf{C}, \mathbf{D}) = \sum (0, 2, 3, 4, 8, 10, 11, 14, 15)$	
		12. Orden the Boolean function $\mathbf{F}(\mathbf{A}, \mathbf{B}, \mathbf{C}, \mathbf{D}) = \sum_{i=1}^{n} (0, 2, 3, 0, 0, 10, 11, 14, 15)$	
		(i) Reduce the above expression by using 4-variable Karnaugh map,	
		(ii) Draw the logic gate diagram for the reduced expression Assume	
		(ii) Draw the togic gate diagram for the reduced expression. Assume that the variables and their complements are available as inputs	
		13 Given the Boolean function $F(P \cap R S) = \pi (5, 7, 8, 10, 12, 14, 15)$	
		(i) Reduce the above expression by using 4-variable Karnaugh map	
		showing the various groups (i.e. octal, quads and pairs).	
		(ii) Draw the logic gate diagram for the reduced expression. Assume that the	
		variables and their complements are available as inputs.	
		14. Simplify the following Boolean expression and draw the gate for the reduced	Ch: Computer Hardware
		expression:	
		F = A'B + AB'C + A	Topic: Combinational Circuits
		15. Define <i>Universal gates</i> . Give one example and show how it works as an OR gate.	https://www.youtube.com/watc
	Ch: Computer	16. Draw the truth table and logic gate diagram for an octal to binary encoder.	<u>h?v=_yHo2qq82P0</u>
	Hardware	17. What is a multiplexer? Also draw the logic diagram of a 4:1 multiplexer.	Tania Maltinlaman
	(Logic Gales	18. Draw the circuit diagram for 3 to 8 decoder.	https://www.wowtube.com/wate
	and then application)	19. A school intends to select candidate for an Inter-School Essay Competition as per	h2v=EKuppyto08A
	application)	the criteria given below: [4]	<u>II ?v=FKvIIIIXte98A</u>
		The student has participated in an earlier competition and is very creative.	
		Or	Topic : Encoder & Decoder
		The student is very creative and has excellent general awareness, but has not	Tople : Elleodel & Decodel
		participated in any competition earlier.	https://www.youtube.com/wate
		Or	h?v=feBvhLFOEDk
		✤ The student has excellent general awareness and has won prize in an enter-house	
		competition.	

		The inputs are:	
		INPUTS	
		A participate in a competition earlier	
		B is very creative	
		D has availant general awareness	
		(in all the above cases 1 indicates yes and 0 indicates no)	
		(in an the above cases 1 indicates yes and 0 indicates no). Output: X [1 indicates yes 0 indicates no for all cases]	
		Draw the truth table for the inputs and outputs given above and write the POS	
		expression for X(A, B, C, D)	
		20 Differentiate between <i>Half Adder</i> and <i>Full Adder</i> . Draw the logic circuit diagram	
		for a Full Adder.	
POL	Ch - 2 : Unitary	Read the chap thoroughly	en.wikipedia.org
SCIENCE	& Federal States		
		Answer the questions according to the syllabus.	
	01.07.20	I. Answer all the questions – Short and Long questions in your notebook.	
	То	NOTE: Write all the new words you have come across while answering the	
	10.07.20	questions.	
	Ch – 3 :	Answer the questions according to the syllabus.	www.youtube.com
	Parliamentary		
	& Presidential	I. Answer all the questions – Short and Long questions in your notebook.	
	forms of		
	Government	NOTE: Write all the new words you have come across while answering the	
		questions	
	13.07.20	Read the chapter thoroughly	
	То		
	22.07.20	VIDEOS WILL BE SEND AS SCHEDULE	

Ch – 10 :	Answer the questions according to the syllabus.	https://www.magnet.brains.co
Fundamental		m
Rights	I. Answer all the questions – Short and Long questions in your notebook.	
	NOTE: Write all the new words you have come across while answering the	
24.07.20	questions	
to		
31.07.20	Read the chapter thoroughly	
	VIDEOS WILL BE SEND AS SCHEDULE	

Rlakshmi

DIRECTOR ACADEMICS